



GP/1756

## PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application Of:

Fujio Kuwako

Application No.: 09/591,523

Filed: June 9, 2000

For: Method For Producing Multi-Layer Printed  
Wiring Boards Having Blind Vias

Atty. Docket No.: 47163-00018USD1

Examiner: Not Assigned

Group Art Unit: 1756

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on December 8, 2000.

Signature

## INFORMATION DISCLOSURE

**STATEMENT UNDER 37 C.F.R. §§ 1.97 AND 1.98**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the references listed on enclosed Form PTO-1449 be considered by the Examiner and made of record. In accordance with 37 C.F.R. § 1.98(d), copies of the listed references are enclosed.

In accordance with 37 C.F.R. §§ 1.97(g) and (h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, or an admission that the information disclosed is, or is considered to be, prior art with respect to the present application or material to patentability, as defined in 37 C.F.R. § 1.56.

This Information Disclosure Statement is being filed prior to receipt of a first Office Action reflecting an examination on the merits and, hence, is believed to be timely in accordance

with 37 C.F.R. § 1.97(b). Accordingly, no fee is believed to be due. Should any fee be deemed necessary (except the issue fee), however, the Commissioner is hereby authorized to charge any such fees which may be required to Deposit Account No. 10-0447/47163-00018USD1. For that purpose, a duplicate copy of this Statement is enclosed.

Respectfully submitted,

Date: 12/8/00



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